ABSTRACT

A method and an apparatus for lossless clock domain translation for a pixel stream have been disclosed. The method includes generating a first pixel stream on a first clock signal, the first clock signal being from a first source, forwarding a second clock signal from a second source and the first pixel stream to a buffer to translate the first pixel stream into a second pixel stream on the second clock signal, and providing a feedback to the second source to cause the second source to adjust the center frequency of the second clock signal to match the average frequency of the first clock signal with the average frequency of the second clock signal.